Part 3

The addition that were added to the Simple Instruction Set Computer were the signals of mm\_sel, wb\_sel, dm\_we and rd\_sel on the control module. These signals allowed to use the load and store which were instructions LDA, LDX, STA and STX. These signals were ultized during steps of excute and memory for store instructions and the additions of decode and writeback for load instructions.

Additionly we had create a new instruction called SWP which uses a modified mux4 and rf module. These modifcations were